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Bantz et al.

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[54] **DIAGNOSTIC/DEBUG MACHINE ARCHITECTURE**

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[57] **ABSTRACT**

A system is described for enabling the connection of a diagnostic/debugging processor to another host processor for the purpose of troubleshooting that processor's hardware and software. The system is composed of an interface between the diagnostic/debugging processor per se and the host processor to be diagnosed, and of software resident in the diagnostic processor to perform functions required by the user of the system. The system is specifically designed for use with a host processor utilizing LSSD design rules.

10 Claims, 10 Drawing Figures